

# ISSCC GLOSSARY

## 1

<b>1P1M</b>	1-Polysilicon layer 1-Metal layer
<b>1T1C</b>	1-Transistor 1-Capacitor

## 3

<b>3D</b>	3-Dimensional
<b>3G</b>	Third-Generation (Wireless)
<b>3T</b>	3-Transistor

## 6

<b>6T</b>	6-Transistor
-----------	--------------

## 8

<b>8T</b>	8-Transistor
-----------	--------------

## $\Delta\Sigma$

$\Delta\Sigma$	Delta-Sigma
$\Delta\Sigma M$	Delta-Sigma Modulator

## $\Sigma\Delta$

$\Sigma\Delta$	$\Delta\Sigma$ is preferred.
$\Sigma\Delta M$	$\Delta\Sigma M$ is preferred.

## A

<b>a-Si</b>	Amorphous Silicon
<b>AC</b>	Alternating Current
<b>A/D</b>	Analog-to-Digital Converter
<b>AAC</b>	Advanced Audio Coding
<b>ACI</b>	Adjacent-Channel Interface
<b>ACLR</b>	Adjacent-Channel Leakage Power Ratio
<b>ACPR</b>	Adjacent-Channel Power Ratio
<b>ADC</b>	Analog-to-Digital Converter
<b>ADDLL</b>	All-Digital DLL
<b>ADPLL</b>	All-Digital PLL
<b>ADSL</b>	Asynchronous Digital Subscriber line
<b>ADU</b>	Analog-to-Digital Unit
<b>AES</b>	Advanced Encryption Standard
<b>AFC</b>	Automatic Frequency Control
<b>AFE</b>	Analog Front End
<b>AGC</b>	Automatic Gain Control
<b>AGU</b>	Address-Generation Unit
<b>AIP</b>	Artificial-Intelligent Partner
<b>ALU</b>	Arithmetic Logic Unit
<b>AM</b>	Amplitude Modulation
<b>AMLCD</b>	Active-Matrix LCD
<b>AMOLED</b>	Active-Matrix OLED
<b>AMP</b>	Asymmetric Multi-Processing
<b>AMPS</b>	Advanced Mobile-Phone Service
<b>AMS</b>	Analog Mixed-Signal {System}
<b>APD</b>	Avalanche Photo-Diode
<b>APG</b>	Algorithmic Pattern Generator
<b>APSK</b>	Amplitude Phase-Shift Keying
<b>ARM</b>	Advanced RISC Machine
<b>ASIC</b>	Application-Specific Integrated Circuit
<b>ASK</b>	Amplitude Shift Keying
<b>ASP</b>	Average Selling Price
<b>ASP</b>	Advanced Simple Profile {MPEG-4 Video}
<b>ATA</b>	Advanced Technology Attachment
<b>ATD</b>	Address-Transition Detection
<b>ATE</b>	Automatic Test Equipment
<b>ATM</b>	Asynchronous Transfer Mode
<b>ATSC</b>	Advanced Television Systems Committee
<b>AVC</b>	Audio-Visual CODEC
<b>AVC</b>	Automatic Volume Control {use AGC}
<b>AWG</b>	Arrayed-Waveguide Grating

## B

<b>BAW</b>	Bulk Acoustic Wave
<b>BB</b>	Baseband
<b>BBT</b>	Band-to-Band Tunneling
<b>BCD</b>	Bipolar-CMOS-DMOS Process
<b>BCD</b>	Binary-Coded Decimal
<b>BCH</b>	Bose-Chaudhuri-Hocquenghem {a type of error-correcting code}
<b>BD</b>	Blu-ray disc
<b>BER</b>	Bit-Error Rate
<b>BGA</b>	Ball-Grid Array
<b>BGR</b>	Band-Gap Reference
<b>BiCMOS</b>	Bipolar Complementary-MOS
<b>BIOS</b>	Basic Input/Output System
<b>BIST</b>	Built-in Self-Test
<b>BJT</b>	Bipolar Junction Transistor
<b>BL</b>	Bitline
<b>BOM</b>	Bill of Materials
<b>BPF</b>	Bandpass Filter
<b>BPSK</b>	Binary Phase-Shift Keying
<b>B-VOP</b>	Bidirectional-Video Object Planes
<b>BW</b>	Bandwidth

## C

<b>C4</b>	Controlled-Collapse Chip Connection
<b>CAD</b>	Computer-Aided Design
<b>CAM</b>	Content-Addressable Memory
<b>CAN</b>	Controller Area Network
<b>CAS</b>	Column-Address Strobe
<b>CCCS</b>	Current-Controlled Current Source
<b>CCD</b>	Charge-coupled Device
<b>CCK</b>	Complementary Code Keying
<b>CCO</b>	Current-Controlled Oscillator
<b>CCVS</b>	Current-Controlled Voltage Source
<b>CDAC</b>	Capacitor DAC
<b>CDMA</b>	Code-Division Multiple Access
<b>CDR</b>	Clock and Data Recovery
<b>CDS</b>	Correlated Double Sampling
<b>CF</b>	Compact Flash
<b>CFA</b>	Color Filter Array
<b>CHE</b>	Channel Hot-Electron (Injection)
<b>CIS</b>	Complex-Instruction-Set Computer
<b>CIS</b>	CMOS Image Sensor
<b>CML</b>	Current-Mode Logic
<b>CMOS</b>	Complementary MOS
<b>CMRR</b>	Common-Mode Rejection Ratio
<b>CMU</b>	Clock Multiplier Unit
<b>CNFET</b>	Carbon Nanotube NFET
<b>CO</b>	Central-Office {hardware}
<b>CODEC</b>	Coder-Decoder
<b>COFDM</b>	Coded FDM
<b>CPE</b>	Customer Premises Equipment
<b>CPU</b>	Central Processing Unit
<b>CPW</b>	CoPlanar Waveguide
<b>CRC</b>	Cyclic Redundancy Check
<b>CSMA</b>	Carrier-Sense Multiple Access
<b>CT</b>	Continuous Time (system)
<b>CUI</b>	Command User Interface
<b>CVD</b>	Chemical Vapor Deposition

## D

<b>D/A</b>	Digital-to-Analog Converter
<b>DAB</b>	Digital-Audio Broadcasting
<b>DAC</b>	Digital-to-Analog Converter
<b>dBFS</b>	dB relative to Full Scale
<b>DBS</b>	Direct-Broadcast Satellite
<b>DCC</b>	Duty-Cycle Corrector

# ISSCC GLOSSARY

<b>DCO</b>	Digitally Controlled Oscillator
<b>DCT</b>	Discrete Cosine Transform
<b>DCVS</b>	Differential Cascode Voltage Switch
<b>DCVS</b>	Digitally-Controlled Voltage Source
<b>DCXO</b>	Digitally Controlled Crystal Oscillator
<b>DDFS</b>	Direct-Digital Frequency Synthesis (or synthesizer)
<b>DDR</b>	Dual Data Rate
<b>DDS</b>	Direct Digital Synthesis
<b>DECT</b>	Digitally Enhanced Cordless Communication
<b>DEM</b>	DEModulator
<b>DEM</b>	Dynamic Element Matching
<b>DEMOS</b>	Depletion MOS
<b>DEMUX</b>	Demultiplexer
<b>DES</b>	Data-Encryption Standard
<b>DEVN</b>	Differential Error Vector Magnitude
<b>DFE</b>	Decision-Feedback Equalizer
<b>DFF</b>	D-type Flip Flop
<b>DFT</b>	Design for Testability
<b>DFT</b>	Discrete Fourier Transform
<b>DFY</b>	Design-for-Yield
<b>DIMM</b>	Dual In-Line Memory Module
<b>DIP</b>	Dual In-line Package
<b>DLL</b>	Delay-Locked Loop
<b>DMA</b>	Direct Memory Access
<b>DMB</b>	Digital Multimedia Broadcasting
<b>DMIPS</b>	Dhrystone Million Instructions Per Second
<b>DMOS</b>	(Double-)Diffused MOS
<b>DNA</b>	Deoxyribonucleic Acid
<b>DNL</b>	Differential Non-Linearity
<b>DNR</b>	Dynamic Range (DR preferred)
<b>DOM</b>	Digital Optical Module
<b>DR</b>	Dynamic Range (see also DNR)
<b>DRAM</b>	Dynamic Random-Access Memory
<b>DRC</b>	Design-Rule Check
<b>DSB</b>	Double Side Band
<b>DSC</b>	Digital Still Camera
<b>DSL</b>	Digital Subscriber Line
<b>DS-OFDM</b>	Direct-Sequence Orthogonal Frequency Division Multiplexing
<b>DSP</b>	Digital Signal Processing
<b>DSSS</b>	Direct-Sequence Spread-Spectrum
<b>DT</b>	Discrete Time
<b>DTL</b>	Diode-Transistor Logic
<b>DTV</b>	Digital Television
<b>DUT</b>	Device Under Test
<b>DVB</b>	Digital-Video Broadcasting
<b>DVB-C</b>	Digital-Video Broadcasting - Cable
<b>DVB-H</b>	Digital-Video Broadcasting - Handhelds
<b>DVB-S</b>	Digital-Video Broadcasting - Satellite
<b>DVB-T</b>	Digital-Video Broadcasting - Terrestrial
<b>DVD</b>	Digital Video Disc
<b>DVS</b>	Dynamic Voltage Scaling
<b>DWA</b>	Data Weighted Averaging
<b>DWDM</b>	Dense-Wavelength-Division Multiplexing
<b>DWMT</b>	Discrete Wavelet Multi-Tone

## E

<b>ECC</b>	Error-Correcting Code
<b>ECG</b>	Electro-CardioGram
<b>ECL</b>	Emitter-Coupled Logic
<b>ECP</b>	Emitter-Coupled Pair
<b>EDGE</b>	Enhanced Data rates for Global Evolution
<b>EDR</b>	Enhanced Data Rate
<b>EEG</b>	Electro-EncephaloGram
<b>EEPROM</b>	Electrically Erasable Programmable Read-Only Memory
<b>EFR</b>	Enhanced Full-Rate (GSM)
<b>EIRP</b>	Effective Isotropic Radiated Power

<b>EKG</b>	Electro-CardioGram (see ECG)
<b>EMG</b>	ElectroMyoGram
<b>EMI</b>	Electro Magnetic Interference
<b>ENOB</b>	Effective Number of Bits
<b>EOT</b>	Electrical Oxide Thickness
<b>EPON</b>	Ethernet-based Passive Optical Network
<b>EPROM</b>	Erasable Programmable Read-Only Memory
<b>ERBW</b>	Effective-Resolution Bandwidth
<b>ESD</b>	ElectroStatic Discharge
<b>EVDO</b>	EVolution Data Optimized (in the context of CDMA)
<b>EVM</b>	Error-Vector Magnitude
<b>EWC</b>	Enhanced Wireless Consortium

## F

$f_{max}$	Unity power gain frequency.
$f_s$	Sampling frequency
$f_t$	Transit frequency
<b>FAMOS</b>	Floating-gate Avalanche-injection MOS Transistor
<b>FBAR</b>	Film Bulk Acoustic Resonator
<b>FBDIMM</b>	Fully Buffered DIMM
<b>FCC</b>	Federal Communications Commission (U.S.)
<b>FDM</b>	Frequency-Division Multiplexing
<b>FDMA</b>	Frequency-Division Multiple-Access
<b>FNDR</b>	Frequency-Dependent Negative Resistor
<b>FEC</b>	Forward Error Checking
<b>FeRAM</b>	Ferro-electric Random Access Memory
<b>FET</b>	Field-Effect Transistor
<b>FF</b>	Flip-Flop
<b>FFE</b>	Feed-Forward Equalizer
<b>FFT</b>	Fast Fourier Transform
<b>FIB</b>	Focused Ion Beam
<b>FIFO</b>	First In-First Out
<b>FinFET</b>	A MOSFET with the gate on two sides (acronym describes the physical shape)
<b>FIR</b>	Finite Impulse Response (filter)
<b>FLOPS</b>	Floating-Point Operations Per Second
<b>FLOTOX</b>	Floating-gate Tunnel Oxide
<b>FM</b>	Frequency Modulation
<b>FMCW</b>	Frequency Modulated Continuous Wave
<b>FN</b>	Fowler-Nordheim
<b>FO4</b>	Fan-Out of 4
<b>FOM</b>	Figure Of Merit
<b>FPGA</b>	Field-Programmable Gate Array
<b>FPN</b>	Fixed Pattern Noise
<b>FPU</b>	Floating Point Unit
<b>FSG</b>	FluoroSilicate glass (dielectric)
<b>FSG</b>	Fluorine-doped Silicate Glass
<b>FSK</b>	Frequency-Shift Keying
<b>FSM</b>	Finite-State Machine

## G

<b>GBW</b>	Gain-BandWidth (product)
<b>GCA</b>	Gain-Controlled Amplifier
<b>GDDR</b>	Graphics Double-Data-Rate
<b>GdFM</b>	Generalized Design-for-Manufacturability
<b>GFSK</b>	Gaussian Frequency-Shift Keying
<b>GFLOPS</b>	Giga Floating-Point Operations Per Second
<b>GIDL</b>	Gate-Induced Drain Leakage
<b>GMSK</b>	Gaussian Minimum-Shift Keying
<b>GOPS</b>	Giga-Operations Per Second
<b>GPRS</b>	General Packet-Radio Service
<b>GPS</b>	Global Positioning System
<b>GSM</b>	Global Standard for Mobile Communication
<b>GUI</b>	Graphical User Interface
<b>GVCO</b>	Gated VCO

# ISSCC GLOSSARY

## H

<b>HBT</b>	Hetero-junction Bipolar Transistor
<b>HBM</b>	Human Body Model
<b>HCI</b>	Host-Controller Interface
<b>HCI</b>	Human-to-Computer Interface
<b>HD</b>	High-Density
<b>HDD</b>	Hard-Disk-Drive
<b>HDL</b>	Hardware-Description Language
<b>HDTV</b>	High-Definition TeleVision
<b>HD2</b>	2 <sup>nd</sup> order Harmonic Distortion
<b>HD3</b>	3 <sup>rd</sup> order Harmonic Distortion
<b>HiFi</b>	High Fidelity
<b>HPF</b>	High-Pass Filter
<b>HSPDA</b>	High-Speed Downlink Packet Access
<b>HT3</b>	Hypertransport 3 (I/O standard)
<b>HTM</b>	Hierarchical Temporal Memory
<b>HV</b>	High Voltage
<b>HVCMOS</b>	High-Voltage Complementary MOS
<b>HVMOS</b>	High-Voltage MOS

## I

<b>I/O</b>	Input-Output
<b>I/Q</b>	In Phase and Quadrature
<b>IBOC</b>	In-Band Out-of-Channel
<b>IC</b>	Integrated Circuit
<b>ID</b>	Identification
<b>IF</b>	Intermediate Frequency
<b>IIP2</b>	Input-referred Input 2 <sup>nd</sup> -order Intercept Point
<b>IIP3</b>	Input-referred Input 3 <sup>rd</sup> -order Intercept Point
<b>IIR</b>	Infinite Impulse Response Filter
<b>IMD</b>	Inter-Modulation Distortion
<b>IM2</b>	2 <sup>nd</sup> -order InterModulation distortion
<b>IM3</b>	3 <sup>rd</sup> -order InterModulation distortion
<b>INL</b>	Integral Non-Linearity
<b>InP</b>	Indium Phosphide
<b>IP</b>	Intellectual Property
<b>IPSEC</b>	Internet (Network) Protocol for Security
<b>IR</b>	Image Rejection
<b>IR</b>	InfraRed
<b>ISDB</b>	Integrated Services Digital Broadcasting
<b>ISDB-C</b>	Integrated Services Digital Broadcasting - Cable
<b>ISDB-S</b>	Integrated Services Digital Broadcasting - Satellite
<b>ISDB-T</b>	Integrated Services Digital Broadcasting - Terrestrial
<b>ISFET</b>	Ion-Sensitive Field-Effect Transistor
<b>ISI</b>	Inter-Symbol Interference
<b>ISM</b>	Industrial, Scientific and Medicine Band
<b>ITU-T</b>	International Telecommunications Union

## J

<b>JPEG</b>	Joint Photographic Expert Group
<b>JTAG</b>	Joint Test-Automation Group

## L

<b>LAGS</b>	Locally-Asynchronous Globally-Synchronous
<b>LAN</b>	Local-Area Network
<b>LBS</b>	Location-Based Services
<b>LCD</b>	Liquid-Crystal Display
<b>LCOS</b>	Liquid-Crystal On Silicon
<b>LDI</b>	LVDS Display Interface
<b>LDCMOS</b>	Laterally Diffused Complementary Metal-Oxide Silicon
<b>LDMOS</b>	Laterally Diffused Metal-Oxide Silicon
<b>LDO</b>	Low Drop-Out (regulator)
<b>LDPC</b>	Low-Density Parity Check
<b>LED</b>	Light-Emitting Diode
<b>LEP</b>	Light-Emitting Polymer
<b>LFCSP</b>	LeadFrame Chip-Scale Package

<b>LFSR</b>	Linear-Feedback Shift Register
<b>LIN</b>	Local Interconnect Network
<b>LMS</b>	Least Mean Square
<b>LNA</b>	Low-Noise Amplifier
<b>LNB</b>	Low-Noise Blockers
<b>LO</b>	Local Oscillator
<b>LPCVD</b>	Low-Pressure Chemical-Vapor Deposition
<b>LPF</b>	Low-Pass Filter
<b>LSB</b>	Least-Significant Bit
<b>LSI</b>	Large-Scale Integration
<b>LTSP</b>	Low-Temperature-Poly Silicon
<b>LV</b>	Low Voltage
<b>LVDS</b>	Low-Voltage Differential Signaling
<b>LVS</b>	Layout Verification to Schematic

## M

<b>MAC</b>	Media-Access Controller
<b>MAC</b>	Multiply-Accumulate
<b>MASH</b>	Multi-stage noise Shaping
<b>MBOA</b>	Multi-Band OFDM Alliance
<b>MB-OFDM</b>	Multi-Band OFDM
<b>MCM</b>	Multi-Chip Module
<b>MCU</b>	MicroController Unit
<b>MDAC</b>	Multiplying DAC
<b>MEMS</b>	Micro-Electro-Mechanical System
<b>MER</b>	Modulation Error Ratio
<b>MfD</b>	Manufacturing-for-Design
<b>microSD</b>	micro Secure Digital
<b>MICS</b>	Medical Implant Communication Band
<b>MIM</b>	Metal-Insulator-Metal
<b>MIMO</b>	Multi-Input, Multiple-Output
<b>MIPI</b>	Mobile Industry Processor Interface
<b>MIPS</b>	Million Instructions Per Second
<b>ML</b>	Matchline
<b>MLC</b>	Multi-Level Cell
<b>MLSD</b>	Maximum-Likelihood-Sequence Detection
<b>MLSE</b>	Maximum-Likelihood-Sequence Estimation
<b>MMAC</b>	Mega Multiply-Accumulate
<b>MMIC</b>	Monolithic Microwave Integrated Circuit
<b>MODEM</b>	Modulator-Demodulator
<b>MOS</b>	Metal-Oxide-Semiconductor (Silicon)
<b>MOSFET</b>	Metal-Oxide-Semiconductor FET
<b>MOST</b>	MOS Transistor
<b>MOST</b>	Media-Oriented Systems Transport
<b>MP</b>	Multi-Processor
<b>MP3</b>	MPeg-1 audio layer 3 (lossy compression algorithm)
<b>MPEG</b>	Motion-Picture Expert Group
<b>Mpps</b>	Million packets per second (VoIP)
<b>MSB</b>	Most Significant Bit
<b>MRAM</b>	Magnetic Random-Access Memory
<b>MRAM</b>	Magnetoresistive Random-Access Memory
<b>MRC</b>	Maximum-Ratio Combining
<b>MSB</b>	Most-Significant Bit
<b>MTJ</b>	Magnetic Tunnel Junction
<b>MTPR</b>	Multi-Tone Power Ratio
<b>MUX</b>	Multiplexer

## N

<b>NBTI</b>	Negative-Bias Temperature Instability
<b>NEF</b>	Noise Efficiency Factor
<b>NEM</b>	Nano-Electro-Mechanical system
<b>NF</b>	Noise Figure
<b>NMOS</b>	N-channel MOS
<b>NMOST</b>	NMOS Transistor
<b>NoC</b>	Network on (a) Chip
<b>NPN</b>	N-type-P-type-N-type bipolar (transistor)
<b>NRTZ</b>	Non Return-To-Zero (see also NRZ)
<b>NRZ</b>	Non-Retun- to-Zero (see also NRTZ)



